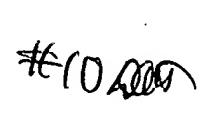


IN THE



UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS:

Mike Hong et al.

SERIAL NO.:

09/313,857

FILING DATE:

May 18, 1999

TITLE:

System and Method for Implementing a Two-Layer Z-Range

Buffer

EXAMINER:

Almis R. Jankus

GROUP ART UNIT:

2671

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on the date printed below:

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9/3/02

Susan Vee

COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

LETTER TO THE CHIEF DRAFTSMAN

SIR:

Subject to the approval of the Primary Examiner in the above-entitled patent application, please substitute the enclosed nineteen (19) sheets of corrected formal drawings containing Figures 1 through 16 for the nineteen (19) sheets of drawings containing Figures 1 through

6/02

16 previously submitted.

Respectfully submitted,

Mike Hong et al.

Dated:

By:

Susan Yee, Reg. No. 41,388

CARR & FERRELL, LLP

2225 East Bayshore Road, Suite 200

Palo Alto, CA 94303

TEL: (650) 812-3400

FAX: (650) 812-3444